

Notice of Allowability

Application No.

10/648,093

Examiner

Shelly A. Chase

Applicant(s)

KIKUCHI ET AL.

Art Unit

2133

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the application filed 8-26-2003.
2. ☒ The allowed claim(s) is/are 1-32.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.


Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


SHELLY CHASE
PRIMARY EXAMINER

DETAILED ACTION

1. Claims 1 to 32 are presented for examination.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119, which papers have been placed of record in the file.

Allowable Subject Matter

3. Claims 1 to 32 are allowed.
4. The following is an examiner's statement of reasons for allowance: the prior art made of record teaches various analysis of low density parity check (LDPC) codes with variable nodes and check nodes for a specific noise threshold. For instance, Vasic et al. (*Irregular low density parity check codes: construction and performance on perpendicular magnetic recording channels*, IEEE) discloses designing LDPC code with degree distribution pairs wherein a splitting technique is used to achieve irregular codes. Varnica et al. (*Optimized LDPC codes for partial response channels*, IEEE) discloses a degree sequence optimization technique to achieve independent and uniformly distributed (I.U.D.) capacities with LDPC codes for a higher noise tolerance. Richardson et al. (*Design of capacity-approaching irregular low density parity check codes*, IEEE) teaches designing LDPC codes with degree distribution pairs for achieving near Shannon capacity.

However, the prior art made of record fails to teach the novel features of the instant invention although the prior art made of record teaches optimizing LDPC codes for some noise threshold. Specifically, the prior art made of record, taken alone or in combination fails to teach or fairly suggest or render obvious the claimed limitation of "the error probability after decoding is minimized for a given variance of noise or the allowable variance of noise is maximized for a given error probability after decoding" as recited in the independent claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

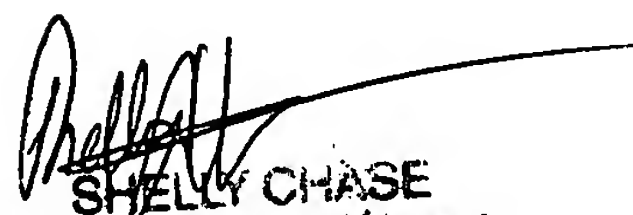
Yedidia et al.(USP 6857097 B2), teaches a method for evaluating and optimizing error correction codes using a renormalization process.

Chung et al. (*Analysis of sum-product decoding of low density parity check codes using a Gaussian approximation*, IEEE), discloses a density evolution algorithm for LDPC codes wherein the exact threshold is computed.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shelly A. Chase whose telephone number is 571-272-3816. The examiner can normally be reached on Mon-Thur from 8:00 am to 6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


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